Ease of implementation and energy efficiency are critical for modern digital ICs. I am researching techniques to improve energy efficiency without compromising on performance or silicon area, especially for CGRA.

**Publications**

- **AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers** *ACM Transactions on Embedded Computing Systems*
  2023; 22 (2)