Stanford



Nikhil Bhagdikar

Ph.D. Student in Electrical Engineering, admitted Autumn 2014

Bio

BIO

Ease of implementation and energy efficiency are critical for modern digital ICs. I am researching techniques to improve energy efficiency without compromising on performance or silicon area, especially for CGRA.

Publications

PUBLICATIONS

• AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers ACM Transactions on Embedded Computing Systems Koul, K., Melchert, J., Sreedhar, K., Truong, L., Nyengele, G., Zhang, K., Liu, Q., Setter, J., Chen, P., Mei, Y., Strange, M., Daly, R., Donovick, et al 2023; 22 (2)