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Bio

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Reza Nasiri Mahalati is an Adjunct Professor in the department of Electrical Engineering at Stanford University and a senior hardware design engineer at Apple Inc. His current work focuses on the development of new hardware technologies that enable more fluid human computer interactions. He received the B.S. degree in Electrical Engineering from the Sharif University of Technology, Tehran, Iran in 2008, and the M.S. and Ph.D. degrees in Electrical Engineering from Stanford University in 2010 and 2013, respectively. While at Stanford, his research focused on mode-division multiplexing in multi-mode optical fibers, fiber-based imaging, optimization and digital signal processing.