



## Suresh Nambi

Masters Student in Electrical Engineering, admitted Autumn 2023

### Bio

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#### BIO

I am a first-year master's student pursuing MS EE (Computer Architecture and Embedded Systems track) at Stanford University.

My research interests lie primarily in the area of Computer Systems Architecture. My research involves the development and implementation of efficient and effective hardware architectures for a given application. I am constantly striving to improve my understanding of computing systems, to develop better intuition of the tradeoffs involved, and make better well-informed hardware architecture decisions.

Before starting my MS, I worked at Nvidia as an ASIC Design Engineer in the GPU Hardware Security team, where I contributed to the development of computer chips used in datacenters. Prior to that I worked at Ceremorphic a stealth startup on their first energy-efficient AI supercomputing test chip and gained insight into their heterogenous computing model.

I completed my undergraduate studies at BITS Pilani, India. During this period, I was associated with Prof. Akash Kumar at the Chair for Processor Design, TU Dresden for my year-long undergraduate thesis. I also had the opportunity to work under Prof. Gerd Grau during my MITACS Globalink Summer Internship.

### Professional

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#### WORK EXPERIENCE

- ASIC Engineer - GPU Hardware Security Team - NVIDIA Corporation (1/3/2022 - 8/18/2023)
- Processor Design Engineer - Machine Learning Subsystem - Ceremorphic, Inc. - Stealth Startup (1/4/2021 - 12/31/2021)
- Guest Researcher - Chair for Processor Design, Technische Universitat Dresden (1/5/2020 - 12/27/2020)