

# Stanford

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## Suyeon Choi

Ph.D. Student in Electrical Engineering, admitted Autumn 2020

### Bio

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#### LINKS

- Personal Site: <https://choisuyeon.github.io/>

### Publications

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#### PUBLICATIONS

- **High-brightness holographic projection.** *Optics letters*  
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- **Partially-Coherent Neural Holography with Fast Spatial Light Modulators**  
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- **Neural 3D Holography: Learning Accurate Wave Propagation Models for 3D Holographic Virtual and Augmented Reality Displays** *ACM TRANSACTIONS ON GRAPHICS*  
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- **Unfiltered holography: optimizing high diffraction orders without optical filtering for compact holographic displays** *OPTICS LETTERS*  
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- **Speckle-free holography with partially coherent light sources and camera-in-the-loop calibration.** *Science advances*  
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- **Optimizing image quality for holographic near-eye displays with Michelson Holography** *OPTICA*  
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- **High-quality holographic displays using double SLMs and camera-in-the-loop optimization**  
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- **Neural Holography with Camera-in-the-loop Training** *ACM TRANSACTIONS ON GRAPHICS*  
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- **Volumetric Head-Mounted Display with Locally Adaptive Focal Blocks.** *IEEE transactions on visualization and computer graphics*  
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- **Neural Holography**  
Peng, Y., Choi, S., Padmanaban, N., Kim, J., Wetzstein, G., ACM  
ASSOC COMPUTING MACHINERY.2020

- **Tomographic Projector: Large Scale Volumetric Display with Uniform Viewing Experiences** *ACM TRANSACTIONS ON GRAPHICS*  
Jo, Y., Lee, S., Yoo, D., Choi, S., Kim, D., Lee, B.  
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- **Optimal binary representation via non-convex optimization on tomographic displays** *OPTICS EXPRESS*  
Choi, S., Lee, S., Jo, Y., Yoo, D., Kim, D., Lee, B.  
2019; 27 (17): 24362–81