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**LINKS**
- Google Scholar: https://scholar.google.com/citations?user=AZhK5nQAAAAJ&hl=en&oi=ao

**Publications**

- **AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers** *ACM Transactions on Embedded Computing Systems*
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- **Canal: A Flexible Interconnect Generator for Coarse-Grained Reconfigurable Arrays** *IEEE Computer Architecture Letters*
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