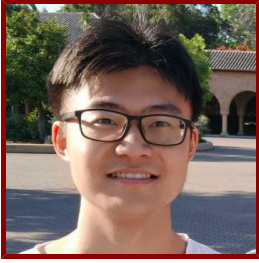


Stanford



Xiangjin Wu

Ph.D. Student in Electrical Engineering, admitted Autumn 2021

Bio

BIO

Xiangjin Wu is a Ph.D. candidate in the Department of Electrical Engineering at Stanford University, co-advised by Prof. Eric Pop and Prof. H.-S. Philip Wong. He received his B.S. in Physics with Honors from Nanjing University in 2020. His research focuses on novel materials and heterostructures for memory applications, including phase change memory (PCM), dynamic random-access memory (DRAM), and their interconnects. Xiangjin is a recipient of the Samsung fellowship.

HONORS AND AWARDS

- Samsung Fellowship, Samsung (September, 2023)
- Best Poster Award, Non-Volatile Memory Technology Symposium (NVMTS) (December, 2022)

EDUCATION AND CERTIFICATIONS

- B.S., Nanjing University, Physics (2020)

LINKS

- Google Scholar: <https://scholar.google.com/citations?user=VyA8OUYAAAAJ&hl=en>
- <http://poplab.stanford.edu/>: <http://poplab.stanford.edu/>
- <https://nano.stanford.edu/>: <https://nano.stanford.edu/>

Publications

PUBLICATIONS

- **Probing the Melting Transitions in Phase-Change Superlattices via Thin Film Nanocalorimetry.** *Nano letters*
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- **Energy Efficient Neuro-inspired Phase Change Memory Based on Ge4 Sb6 Te7 as a Novel Epitaxial Nanocomposite.** *Advanced materials (Deerfield Beach, Fla.)*
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- **First Demonstration of Ge₂Sb₂Te₅-Based Superlattice Phase Change Memory with Low Reset Current Density (~3 MA/cm²) and Low Resistance Drift (~0.002 at 105°C)** *IEEE Symposium on VLSI Technology and Circuits (VLSI Technology and Circuits)*
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Li, W., Zhou, J., Cai, S., Yu, Z., Zhang, J., Fang, N., Li, T., Wu, Y., Chen, T., Xie, X., Ma, H., Yan, K., Dai, et al
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