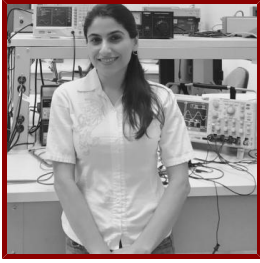


Stanford



Shirin Pourashraf

Postdoctoral Research Fellow, Molecular Imaging Program at Stanford

Bio

BIO

Shirin Pourashraf is from Darreh Shahr, Ilam, Iran. She received the M.S. degree from the Department of Electrical and Computer Engineering, Isfahan University of Technology, Isfahan, Iran, in 2011, and the Ph.D. degree from New Mexico State University, Las Cruces, NM, USA, in 2018.

Her field of studies was the design and test of low-voltage/low-power and high performance analog/digital/mixed-signal building blocks.

She authored several journal papers during her Ph.D., and was the recipient of the Outstanding Teaching Assistant Award from the Klipsch School of Electrical and Computer Engineering, New Mexico State University, in 2017 and the Outstanding Graduate Assistantship Award from the Graduate School of New Mexico State University in 2018.

Dr. Pourashraf is currently a Postdoctoral Fellow Scholar in the Department of Radiology, Molecular Imaging Instrumentation Laboratory (MIIL) working on TOF-PET Imaging; specifically exploring, and designing instrumentation, and data acquisition electronics to improve time-of-flight resolution in PET detectors.

HONORS AND AWARDS

- Outstanding Graduate Assistantship Award, Graduate School, New Mexico State University (2018)
- Outstanding Teaching Assistant Award, Klipsch School of Electrical and Computer Engineering Department, New Mexico State University (2017)
- Talented Students Association Award, Shahid Chamran University of Ahvaz (2002)

PROFESSIONAL EDUCATION

- Doctor of Philosophy, New Mexico State Univ, Las Cruces (2018)
- Master of Science in Engr, Isfahan University Of Technology (2011)
- Bachelor of Elec Engineering, Shahid Chamran University (2007)

STANFORD ADVISORS

- Craig Levin, Postdoctoral Faculty Sponsor

LINKS

- Molecular Imaging Instrumentation Laboratory: <http://med.stanford.edu/miil.html>
- LinkedIn: <https://www.linkedin.com/in/shirin-pourashraf-802a1750/>
- Google Scholar: https://scholar.google.com/citations?hl=en&user=p9rILLAAAAAJ&view_op=list_works&sortby=pubdate
- ResearchGate: https://www.researchgate.net/profile/Shirin_Pourashraf/research
- Google+: <https://plus.google.com/u/0/109040264139130097165?tab=iX>

Research & Scholarship

CURRENT RESEARCH AND SCHOLARLY INTERESTS

I am working on TOF-PET Imaging; specifically exploring, and designing instrumentation, and data acquisition electronics to improve time-of-flight resolution in PET detectors.

Publications

PUBLICATIONS

- **Gain and Bandwidth Enhanced Class-AB OTAs**
Pourashraf, S., Ramirez-Angulo, J., Roman-Loera, A., Gangineni, M.
2019 IEEE 62th International Midwest Symposium on Circuits and Systems (MWSCAS).2019
- **Ultra Low Voltage Gate Driven Bandpass PGA with Constant Bandwidth.**
Pourashraf, S., Ramirez-Angulo, J., Díaz-Sánchez, A.
2018 IEEE International Symposium on Circuits and Systems (ISCAS).2018
- **Continuous and Discrete Time Low Voltage Analog Circuits in 16 nm CMOS Technology.**
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2018 IEEE International Symposium on Circuits and Systems (ISCAS).2018
- **An Amplified Offset Compensation Scheme and its Application in a Track and Hold Circuit.** *IEEE Transactions on Circuits and Systems II (TCAS-II)*
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- **An Op-amp Approach for Bandpass VGAs with Constant Bandwidth.** *IEEE Transactions on Circuits and Systems II (TCAS-II)*
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- **A Highly Efficient Composite Class-AB-AB Miller Op-amp with High Gain and Stable from 15 pF up to Very Large Capacitive Loads.** *IEEE Transaction on Very Large Scale Integrated Systems (TVLSI)*
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2018; 26 (10): 2061 - 2072
- **±0.25 V Low-voltage Class-AB CMOS Capacitor Multiplier and Precision Rectifiers.** *IEEE Transaction on Very Large Scale Integrated Systems (TVLSI)*
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- **±0.18 V Supply Gate Driven PGA with 0.7 Hz to 2 kHz Constant Bandwidth and 0.15 μW Power Dissipation.** *International Journal of Circuit Theory and Application (IJCTA)*
Pourashraf, S., Ramirez-Angulo, J., Lopez#Martin, A. J., González-Carvajal, R., Díaz#Sánchez, A.
2017; 46 (2): 272-279
- **Offset compensation in a track and hold circuit.**
Pourashraf, S., Ramirez-Angulo, J., Cabrera-Galicia, A. R., Lopez-Martin, A. J., González-Carvajal, R.
2017 IEEE 60th International Midwest Symposium on Circuits and Systems (MWSCAS).2017
- **A super class-AB OTA with high output current and no open loop gain degradation.**
Pourashraf, S., Ramirez-Angulo, J., Lopez-Martin, A. J., González-Carvajal, R.
2017 IEEE 60th International Midwest Symposium on Circuits and Systems (MWSCAS).2017
- **Super Class-AB OTA without Open Loop Gain Degradation Based on Dynamic Cascode Biasing.** *International Journal of Circuit Theory and Application (IJCTA)*
Pourashraf, S., Ramirez-Angulo, J., Lopez-Martin, A. J., González-Carvajal, R.
2017; 45 (5): 2111-2118
- **High current efficiency class-AB OTA with high open loop gain and enhanced bandwidth.** *IEICE Letters, Electronics Express*

Pourashraf, S., Ramirez-Angulo, J., Roman-Loera, A., Lopez-Martin, A. J., Diaz-Sanchez, A., González-Carvajal, R.
2017; 14 (17): 20170719

- **Implementation of a Low Power 16-bit Radix-4 Pipelined SRT Divider Using a Modified Data Driven Dynamic Logic (D3L) Structure.** *Microelectronics Journal*

Pourashraf, S., Sayedi, S.
2013; 44 (12): 1165-1174

- **A low power D3L 16-bit radix- 4 pipelined SRT divider.**

Pourashraf, S., Sayedi, S.
2012 25th IEEE Canadian Conference on Electrical and Computer Engineering (CCECE).2012

- **A novel 4#2 compressor for high speed and low power applications.**

Pourashraf, S., Sayedi, M.
2010 18th IEEE Iranian Conference on Electrical Engineering.2010