Stanford

Daniel Stanley

Ph.D. Student in Electrical Engineering, admitted Autumn 2018

Bio

BIO

Daniel is a PhD student currently working on tools for validating mixed-signal systems. He received his bachelor's degree from Princeton University in 2018. His research interests include designing analog and digital hardware as well as creating tools that make hardware design faster and easier.

Publications

PUBLICATIONS

- **fault: A Python Embedded Domain-Specific Language for Metaprogramming Portable Hardware Verification Components** *International Conference on Computer Aided Verification*
  Truong, L., Herbst, S., Setaluri, R., Mann, M., Daly, R., Zhang, K., Donovick, C., Stanley, D., Horowitz, M., Barrett, C., Hanrahan, P.
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