

# Stanford

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Ph.D. Student in Electrical Engineering, admitted Autumn 2020

### **Publications**

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#### **PUBLICATIONS**

- **Illusion of large on-chip memory by networked computing chips for neural network inference** *NATURE ELECTRONICS*  
Radway, R. M., Bartolo, A., Jolly, P. C., Khan, Z. F., Le, B. Q., Tandon, P., Wu, T. F., Xin, Y., Vianello, E., Vivet, P., Nowak, E., Wong, H., Aly, et al  
2021