# Stanford



## Maxwell Bradley Strange

Ph.D. Student in Electrical Engineering, admitted Autumn 2018

### Bio

#### BIO

Max is a Ph.D. student in Electrical Engineering advised by Mark Horowitz. His research focuses on developing infrastructure and tools to facilitate agile hardware development as part of the ongoing efforts by the Stanford AHA! Research Center. His research interests also include domain-specific hardware architectures, hardware/software co-design, and embedded systems design. He graduated from the University of Wisconsin in 2017 with a B.S. in Computer Engineering and Computer Science.

#### **Publications**

#### **PUBLICATIONS**

- AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers ACM Transactions on Embedded Computing Systems Koul, K., Melchert, J., Sreedhar, K., Truong, L., Nyengele, G., Zhang, K., Liu, Q., Setter, J., Chen, P., Mei, Y., Strange, M., Daly, R., Donovick, et al 2023; 22 (2)
- Unified Buffer: Compiling Image Processing and Machine Learning Applications to Push-Memory Accelerators ACM Transactions on Architecture and Code Optimization

Liu, Q., Setter, J., Huff, D., Strange, M., Feng, K., Horowitz, M., Raina, P., Kjolstad, F. 2023: 26