

Stanford



Gedeon Nyengele

Ph.D. Student in Electrical Engineering, admitted Autumn 2017

Bio

PERSONAL INTERESTS

Bodybuilding & fitness

Publications

PUBLICATIONS

- **AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers** *ACM Transactions on Embedded Computing Systems*
Koul, K., Melchert, J., Sreedhar, K., Truong, L., Nyengele, G., Zhang, K., Liu, Q., Setter, J., Chen, P., Mei, Y., Strange, M., Daly, R., Donovanick, et al
2023; 22 (2)
- **Creating an Agile Hardware Design Flow**
Bahr, R., Barrett, C., Bhagdikar, N., Carsello, A., Daly, R., Donovanick, C., Durst, D., Fatahalian, K., Feng, K., Hanrahan, P., Hofstee, T., Horowitz, M., Huff, et al
IEEE.2020
- **Optimized Gateway Placement for Interference Cancellation in Transmit-Only LPWA Networks.** *Sensors (Basel, Switzerland)*
Tian, H., Weitnauer, M. A., Nyengele, G.
2018; 18 (11)