

Stanford

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Publications

PUBLICATIONS

- A 1024-Channel 268-nW/Pixel 36 x 36 μm ²/Channel Data-Compressive Neural Recording IC for High-Bandwidth Brain-Computer Interfaces *IEEE JOURNAL OF SOLID-STATE CIRCUITS*
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- A CMOS-based highly scalable flexible neural electrode interface. *Science advances*
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- Direct-print three-dimensional electrodes for large- scale, high-density, and customizable neural inter- faces. *bioRxiv : the preprint server for biology*
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- Automated and Wireless Accelerated Heat Soak Testing System to Assess Hermetic Failure Mechanism of Inductively Powered Implantable Medical Applications *ADVANCED MATERIALS TECHNOLOGIES*
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- A scalable bonding technique for the development of next-generation brain-machine interfaces
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