



## David Durst

Ph.D. Student in Computer Science, admitted Autumn 2017

### Bio

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#### BIO

David is a Computer Science PhD candidate at Stanford University. He's advised by Kayvon Fatahalian and Pat Hanrahan and affiliated with the AHA Agile Hardware Center. His research focuses on programming languages and computer architecture. He's supported by an NSF Graduate Research Fellowship and a Stanford Graduate Fellowship in Science and Engineering. Previously, he worked at BlackRock as a Financial Modeling Group Analyst and received a B.S.E. in Computer Science from Princeton University in 2015.

### Publications

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#### PUBLICATIONS

- **AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers** *ACM Transactions on Embedded Computing Systems* Koul, K., Melchert, J., Sreedhar, K., Truong, L., Nyengele, G., Zhang, K., Liu, Q., Setter, J., Chen, P., Mei, Y., Strange, M., Daly, R., Donovanick, et al 2023; 22 (2)
- **Type-Directed Scheduling of Streaming Accelerators** Durst, D., Feldman, M., Huff, D., Akeley, D., Daly, R., Bernstein, G., Patrignani, M., Fatahalian, K., Hanrahan, P., Donaldson, A. F., Torlak, E. ASSOC COMPUTING MACHINERY.2020: 408–22
- **Creating an Agile Hardware Design Flow** Bahr, R., Barrett, C., Bhagdikar, N., Carsello, A., Daly, R., Donovanick, C., Durst, D., Fatahalian, K., Feng, K., Hanrahan, P., Hofstee, T., Horowitz, M., Huff, et al IEEE.2020