

Stanford



Alexander Carsello

Ph.D. Student in Electrical Engineering, admitted Autumn 2017

Bio

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Alex is currently a Ph.D. student in Electrical Engineering advised by Mark Horowitz and affiliated with the AHA! Agile Hardware Center. He is interested in reconfigurable computing, domain-specific architectures for image processing, and hardware design methodology. He is currently working within the AHA Agile Hardware Project on a next-generation CGRA (coarse-grained reconfigurable architecture) chip generator. Alex received a B.S. in Electrical and Computer Engineering from Washington University in St. Louis in 2017.

EDUCATION AND CERTIFICATIONS

- B.S., Washington University in St. Louis , Electrical Engineering (2017)
- B.S., Washington University in St. Louis , Computer Engineering (2017)