

# Stanford

---

## John Edward Watkins

Ph.D. Student in Electrical Engineering, admitted Autumn 2020

### Publications

---

#### PUBLICATIONS

- **Combinational Logic Design Using Six-Terminal NEM Relays** *IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS*  
Lee, D., Lee, W. S., Chen, C., Fallah, F., Provine, J., Chong, S., Watkins, J., Howe, R. T., Wong, H. P., Mitra, S.  
2013; 32 (5): 653-666