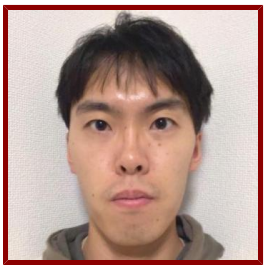
Stanford



Shota Suzuki

Graduate Visiting Researcher Student, Electrical Engineering

Bio

BIO

Shota Suzuki is a Visiting Student Researcher in the Tambe Lab within the Department of Electrical Engineering at Stanford University. He is concurrently pursuing his Ph.D. in Electrical Engineering and Information Systems at The University of Tokyo, advised by Prof. Ken Takeuchi. His work sits at the intersection of computer architecture, circuit design, emerging non-volatile memory devices, and hardware-software co-design for AI workloads.

His current research explores non-volatile memory (ReRAM) optimization for LLM KV cache management, dynamic approximate computing, and heterogeneous compute partitioning across memory hierarchies. As Principal Investigator for the MEXT SPReAD program, he leads research on power-efficient AI hardware via agent-driven autonomous Processing-in-Memory (PIM) design.

HONORS AND AWARDS

- Principal Investigator (PI), MEXT SPReAD Program, Ministry of Education, Culture, Sports, Science and Technology (2026)
- JST SPRING GX Fellow, Advanced Human Resource Development Leading Green Transformation, JST (2026 – 2029)
- SLDM Excellent Presentation Award, Information Processing Society of Japan (IPSJ) (2026)
- Qualcomm Special Prize, UTokyo AI Program Hackathon (2026)
- Best Research Award, 8th Cross-Disciplinary Workshop on Computing Systems, Infrastructures, and Programming (xSIG) (2024)

EDUCATION AND CERTIFICATIONS

- Master of Engineering, The University of Tokyo , Electrical Engineering and Information Systems (2026)
- Bachelor of Engineering, The University of Tokyo , Electrical and Electronic Engineering (2024)

LINKS

- https://suzuki-shota3518.github.io/en/: https://suzuki-shota3518.github.io/en/

Research & Scholarship

LAB AFFILIATIONS

- Thierry Tambe, Tambe Lab (11/1/2026 - - 11/30/2026)