

Stanford



Thibaut Van Gorp

Graduate Visiting Researcher Student, Aeronautics and Astronautics

Bio

BIO

Thibaut received his Bachelor of Science in Electrical and Mechanical Engineering from UCLouvain (Belgium). His interests include semiconductor device modeling and characterization, with a focus on micro- and nanofabrication, RF applications, and electronics for harsh environments.

He is currently a Graduate Visiting Researcher at XLab, Stanford University, while completing his Master of Science in Electrical Engineering at UCLouvain.

His Master's thesis, titled "Characterization, Modeling, and Simulation of FD-SOI Transistors at High Temperature," focuses on the analysis of DC and RF device performance using TCAD simulations and compact modeling in Cadence Virtuoso.

SERVICE, VOLUNTEER, AND COMMUNITY WORK

- Student Volunteer at EUMW2025 (9/20/2025 - 9/28/2025)

LINKS

- LinkedIn profile: <https://www.linkedin.com/in/thibaut-van-gorp/>

Research & Scholarship

LAB AFFILIATIONS

- Debbie Senesky, XLab (4/9/2026 - - 7/8/2026)

Professional

WORK EXPERIENCE

- Visiting Graduate Researcher at MIT - MIT (Massachusetts Institute of Technology) (7/1/2025 - 8/31/2025)
- Intern at imec - imec (7/1/2024 - 8/31/2024)
- Teaching Assistant at UCLouvain - Université Catholique de Louvain (9/1/2022 - present)