



## Chik Patrick Yue

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### Bio

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#### BIO

Prof. Yue received the Master and PhD degrees in EE from Stanford in 1994 and 1998, respectively. He has been a professor for over two decades, and taught IC design classes and conduct research at HKUST (2010-now), UC Santa Barbara (2006-11), Carnegie Mellon (2003-06), Tsinghua (visiting 2016), and Stanford (visiting 1998 & 2025-now). He has supervised over 10 post-docs, 30 PhD and 10 MPhil students. He has published over 250 technical papers, holds over 25 patents and accumulated over 10000 citations. Together with his students and teachers, he has been awarded the IEEE VLSI Circuit Symposium Test of Time Award (2024), the IEEE Circuits and Systems Society Outstanding Young Author Award (2017), and the Guanghua Engineering Science and Technology Youth Award by the Chinese Academy of Engineering (2016), and the ISSCC Best Student Paper Award (2003). Over the years, he has cofounded a number of startups in both Silicon Valley and Hong Kong including Atheros Communications (1998), Jetcomm Technologies (2014), LiPHY Communications (2018), and High5 Semiconductor (2024) to commercialize technologies from academic to industry. Prof. Yue is a Fellow of the IEEE and Optica, and a member of the ACM. He has a diverse research interests spanning from optical wireline and mmWave wireless SoC and SiP design, neural implant microsystems, 3D computer vision models, and power network management system.