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Bio

BIO

Haotian Su is a Ph.D. candidate in Electrical Engineering at Stanford University, co-advised by Prof. Eric Pop and Prof. Shan X. Wang. He received his B.S. in EE from National University of Singapore (2022). His research focuses on developing novel materials and thin films for energy-efficient memories and computing, including magnetic random-access memory (MRAM), oxide transistors, and other nanoscale devices.

EDUCATION AND CERTIFICATIONS

- B.S., National University of Singapore , Electrical Engineering (2022)
- M.S., Stanford University , Electrical Engineering (2025)

LINKS

- Poplab: https://poplab.stanford.edu/
- WangGroup: https://wanggroup.stanford.edu/
- Google Scholar: https://scholar.google.com/citations?user=bPqVdqcAAAAJ&hl=en
- LinkedIn: https://www.linkedin.com/in/haotian-su-b63b8a1a2/

Publications

PUBLICATIONS

- **Scaling nanoribbon transistors with monolayer transition metal dichalcogenides.** *Nature nanotechnology*
  Peña, T., Persson, A. E., Krayev, A., Friðriksdóttir, Á., Su, H., Lee, Y. M., Song, Y. S., Neilson, K., Zhang, Z., Hoang, A. T., Yang, J. A., Hoang, L., Wang, et al
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  2025
- **Thermal Evaluation and Comparison of CAA and GAA Indium Tin Oxide Vertical Channel Transistors**
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- **First Comparative Thermal Evaluation of 2D Semiconductor vs. Silicon Nanosheet Transistors**
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  Su, H., Kwon, H., Hwang, W., Xue, F., Koroglu, C., Tsai, W., Asheghi, M., Goodson, K. E., Wang, S. X., Pop, E.
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