

# Stanford

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## Dawood Alnajjar

Software Dvlpr 3, SLAC National Accelerator Laboratory

### Bio

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#### BIO

A passionate individual with a masters and PhD in Information Systems Engineering, and with more than 12 years of international professional experience in FPGA prototyping, embedded system development, reverse engineering, digital circuit design, verification, and layout, low-level software, library and driver development, optimization, high performance computing, research, and demo development.

Currently, landed a job in the Stanford University Linear Accelerator Center as a Senior Embedded Systems Software Engineer, working with embedded system development and FPGA prototyping.