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- Ph.D. Minor, Computer Science

Publications

PUBLICATIONS

- **G-QED: Generalized QED Pre-silicon Verification beyond Non-Interfering Hardware Accelerators**
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- **LeGO: A Learning-Guided Obfuscation Framework for Hardware IP Protection** *IEEE TRANSACTIONS ON COMPUTER-AIDED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS*
Alaql, A., Chattopadhyay, S., Chakraborty, P., Hoque, T., Bhunia, S.
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- **Proof Stitch: Proof Combination for Divide-and-Conquer SAT Solvers**
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- **A Conditionally Chaotic Physically Unclonable Function Design Framework with High Reliability** *ACM TRANSACTIONS ON DESIGN AUTOMATION OF ELECTRONIC SYSTEMS*
Chattopadhyay, S., Santikellur, P., Chakraborty, R., Mathew, J., Ottavi, M.
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- **A-QED Verification of Hardware Accelerators**
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- **Cyclic Beneg Network based Logic Encryption for Mitigating SAT-Based Attacks**
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- **Machine Learning Assisted Accurate Estimation of Usage Duration and Manufacturer for Recycled and Counterfeit Flash Memory Detection**
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- **A novel technique for implementing reactance in spin domain using Spin Hall Effect** *MICROELECTRONICS JOURNAL*
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- **STT-MRAM for Low Power Access for Read-Intensive Parallel Deep-Learning Architectures**
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