Bio

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Kavya is an electrical engineering PhD student advised by Mark Horowitz. She is interested in architecture design and developing hardware accelerators that efficiently support a variety of image processing and machine learning applications. Her current research is with the AHA! Project in developing Lake, a parameterizable memory generator that can be configured at runtime to support different applications. She is supported by Stanford's Knight-Hennessy scholarship and holds a B.S. in electrical engineering and BEM (Business, Economics, & Management) from Caltech in 2019.

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• Mark Horowitz, Doctoral (Program)