

Stanford



Aya Mouallem

- Ph.D. Student in Electrical Engineering, admitted Autumn 2020
- Master of Arts Student in Education, admitted Summer 2025

Bio

HONORS AND AWARDS

- DARE Fellow, Stanford VPGE (2024-2026)
- RAISE Fellow, Stanford VPGE (2022-2025)
- Knight-Hennessy Scholar, Stanford University (2020-2023)

LINKS

- Personal Website: <https://www.ayamouallem.com>

Publications

PUBLICATIONS

- **Promoting Comprehension and Engagement in Introductory Data and Statistics for Blind and Low-Vision Students: A Co-Design Study**
Fan, D., Tomassetti, O., Mouallem, A., Kim, G., Patel, S., Hwang, S., Leader, P., Sugrue, D., Chen, T., Ou, D., Lee, V. R., Balasubramanian, L., Subramonyam, et al
ASSOC COMPUTING MACHINERY.2025
- **IncluSim: An Accessible Educational Electronic Circuit Simulator for Blind and Low-Vision Learners**
Mouallem, A., Pons, M., Malik, A., Rogando, T., Kim, G., Kulkarni, T., Chong, C., Fan, D., Patel, S., Shluzas, L., Chen, H. L., Sheppard, S. D., ACM
ASSOC COMPUTING MACHINERY.2025
- **A Case for Improving the Accessibility of Electrical and Computer Engineering Education - Starting with a Blind Student's Autoethnography**
Kulkarni, T., Kim, G., Mouallem, A., ACM
ASSOC COMPUTING MACHINERY.2023