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Bio

LINKS

- Google Scholar: <https://scholar.google.com/citations?hl=en&user=lvjzwxYAAAAJ>

Publications

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- **Monolithic 3-D Integration of Diverse Memories: Resistive Switching (RRAM) and Gain Cell (GC) Memory Integrated on Si CMOS** *IEEE TRANSACTIONS ON ELECTRON DEVICES*
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- **Modeling and Understanding Threshold Voltage and Subthreshold Swing in Ultrathin Channel Oxide Semiconductor Transistors**
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