

# Stanford

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## Rajat Vadiraj Dwaraknath

- Ph.D. Student in Computational and Mathematical Engineering, admitted Autumn 2021
- Masters Student in Computational and Mathematical Engineering, admitted Winter 2026

### Bio

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#### LINKS

- Personal Site: <https://rajatvd.github.io/>

### Publications

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#### PUBLICATIONS

- **Fixing the NTK: From Neural Network Linearizations to Exact Convex Programs**  
Dwaraknath, R., Ergen, T., Pilanci, M.  
edited by Oh, A., Neumann, T., Globerson, A., Saenko, K., Hardt, M., Levine, S.  
NEURAL INFORMATION PROCESSING SYSTEMS (NIPS).2023
- **Towards Optimal Effective Resistance Estimation**  
Dwaraknath, R., Karmarkar, I., Sidford, A.  
edited by Oh, A., Neumann, T., Globerson, A., Saenko, K., Hardt, M., Levine, S.  
NEURAL INFORMATION PROCESSING SYSTEMS (NIPS).2023