Stanford

Atindra Jha

Ph.D. Student in Electrical Engineering, admitted Autumn 2026

Bio

BIO

I am a PhD student advised by Jure Leskovec and Percy Liang. I work on efficient and performant large-scale distributed data processing (currently, agentic and mixed-modal inference), natural language processing, and machine learning. I also work with Stephanie Wang, Baris Kasikci, and Luke Zettlemoyer at the University of Washington. I completed by B.S. and M.S. in Computer Science at Stanford, advised by Prof. Mark Horowitz, and affiliated with the Portal Center, AHA! Agile Hardware Center, and the Stanford VLSI Group.

Research & Scholarship

LAB AFFILIATIONS

- Mark Horowitz, AHA! Agile Hardware Center (9/25/2023)
- Mark Horowitz, Stanford VLSI Research Group (2/15/2020)