



Taeyoung Kong

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Bio

BIO

Taeyoung is a Ph.D. student at Stanford University working with prof. Mark Horowitz in VLSI group and he is currently working within the AHA Agile Hardware Project. He is interested in hardware accelerator for deep learning / image processing and hardware design methodology. Taeyoung received a B.S. in Electrical and Computer Engineering from Seoul National University in 2017, and M.S. in Electrical Engineering from Stanford University in 2020.

Publications

PUBLICATIONS

- **Amber: A 16-nm System-on-Chip With a Coarse-Grained Reconfigurable Array for Flexible Acceleration of Dense Linear Algebra** *IEEE JOURNAL OF SOLID-STATE CIRCUITS*
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- **AHA: An Agile Approach to the Design of Coarse-Grained Reconfigurable Accelerators and Compilers** *ACM Transactions on Embedded Computing Systems*
Koul, K., Melchert, J., Sreedhar, K., Truong, L., Nyengele, G., Zhang, K., Liu, Q., Setter, J., Chen, P., Mei, Y., Strange, M., Daly, R., Donovanick, et al
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- **Creating an Agile Hardware Design Flow**
Bahr, R., Barrett, C., Bhagdikar, N., Carsello, A., Daly, R., Donovanick, C., Durst, D., Fatahalian, K., Feng, K., Hanrahan, P., Hofstee, T., Horowitz, M., Huff, et al
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