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- Ph.D. Student in Electrical Engineering, admitted Autumn 2016
- Other Tech - Graduate, Stanford Nano Shared Facilities

Publications

PUBLICATIONS

- Application-driven synthesis and characterization of hexagonal boron nitride deposited on metals and carbon nanotubes. *2D MATERIALS*
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- High-performance flexible nanoscale transistors based on transition metal dichalcogenides. *NATURE ELECTRONICS*
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- Ultrathin Three-Monolayer Tunneling Memory Selectors. *ACS nano*
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- Engineering Thermal Transport across Layered Graphene-MoS2 Superlattices. *ACS nano*
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- Advanced Data Encryption using 2D Materials. *Advanced materials (Deerfield Beach, Fla.)*
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- Monolithic mtesla-level magnetic induction by self-rolled-up membrane technology. *Science advances*
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- Contact Engineering High-Performance n-Type MoTe2 Transistors. *Nano letters*
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- Electronic synapses made of layered two-dimensional materials. *NATURE ELECTRONICS*
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- 3D Monolithic Stacked 1T1R cells using Monolayer MoS2 FET and hBN RRAM Fabricated at Low (150 degrees C) Temperature
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